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PATENTS  
ELM/002 Cont. 4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant : Glenn J. Leedy

Application No. : 10/672,961 Confirmation No.: 9439

Filed : September 26, 2003

For : THREE DIMENSIONAL MULTI LAYER MEMORY AND  
CONTROL LOGIC INTEGRATED CIRCUIT STRUCTURE

Art Unit : 2822

Examiner : Monica Lewis

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

REPLY TO FINAL OFFICE ACTION

Sir:

In reply to the July 26, 2007 Office Action, applicant  
hereby amends the above-identified patent application as follows:

Amendments to the Claims begin on page 2 of this Reply.

Remarks begin on page 13 of this Reply.